

Slide 1

### Administrivia

- Scheduled times for the final are May 6 (Wednesday) at 8:30am (section 1) and May 7 (Thursday) at noon (section 2). You can show up at either time.  
*OR* I am considering making it a take-home exam instead, as described in e-mail. (Let's discuss, but after review of other administrivia?)  
Format/rules will be roughly similar to the midterm, but longer and (somewhat) cumulative. Review sheet describing format and topics on the Web.
- Reminder: Homework 5 due next Thursday. (Last-minute clarification to problem 2 just posted. Look for "ADDITION".)  
We need a "not accepted past" date for this and other homeworks. How about end of reading days for everything other than Homework 5, and next Friday for that?
- I will post extra-credit problems as soon as I can, no later than next Monday. To be due end of the last day of exams (May 12). Can only help your grade.

Slide 2

### More Administrivia

- Sample solutions to quizzes online.
- Sample solutions to midterms distributed in hardcopy.
- Sample solutions for homeworks on Google Drive (first two now, other two very soon).
- I'm intending to have office hours next week but am not sure when. I will send mail when I know.
- Should there be a review session? Next Monday seems like the only workable time, late afternoon? if there is interest we can pick a time by e-mail.
- (Discussion of in-class versus take-home exam.)
- (Look briefly at review sheet; continue after evaluations?)

### Course Recap — Topics

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- A little about performance.
- MIPS assembler language.
- Translating C to MIPS assembler language.
- Binary representation of instructions.
- Binary representation of data (integers, ASCII, floating-point numbers).
- Computer arithmetic.
- Gate-level logic design.
- Design of a processor — ALU, datapath, control.
- Other schools spread this material (plus some, okay) over two or even three courses! so, we have done a lot?

### Course Recap, Continued

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- Some topics — representation of data, computer arithmetic, maybe finite stat machines — are review, or small extension of what you know.
- Others, though — assembler language, gate-level logic, designing a processor in terms of AND/OR/NO and how it works — are not familiar to most, and involve a new perspective, or mindset, or “mental model”?

## Minute Essay

- None really — sign in. If you have administrivia or other questions, though, this might be a good time to ask.

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