

Minute Essay From Last Lecture

 Responses about loop invariants varied — some people didn't remember hearing about them at all, others remembered only vaguely. I think they're a useful way of thinking about loops. I have an overview in my slides from last time.

Slide 2

• Most people had some exposure to multithreading. My impression is that it's often mentioned in CS2, Data Abstraction, and Algorithms.



Mutual Exclusion Problem — Recap So far we looked at a few proposed solutions that didn't work, for various reasons. Now we'll look at some that do (yay!).



Peterson's Algorithm, Continued
Intuitive idea: p0 can only start do_cr() if either p1 isn't interested, or p1 is interested but it's p0's turn; turn "breaks ties".
Semi-formal proof using invariants is a bit tricky. Proposed invariant has two parts:

"If p0 is in its critical region, interested0 is true and either interested1 is false or turn is 1"; similarly for p1.
"turn is either 0 or 1."

If we can show that, first requirement (no more than one process in critical region) is true. Other requirements are too.

Second part is clearly okay, but for the first, a fiddly detail — the invariant can be false if p0 is in its critical region when p1 executes the lines interested1 = true; turn = 1;. So revise a bit...



Peterson's Algorithm, Continued
Revised invariant again: "If p0 is in its critical region, interested0 is true and one of the following is true: interested1 is false, turn is 1, or p1 is between L1 and L2", and similarly for p1. Invariant?
True initially.
Could change when either process enters its critical region. But this only happens ... when? So okay.
Doesn't change when eiher process leaves its critical region (somewhat trivially).
Changes to interesten — this is where the revision comes in; if the other process is in its critical region then it's a bit fiddly, but okay with revision.
Changes to turn are okay.
So okay!



Sidebar: TSL Instruction
A key problem in concurrent algorithms is the idea of "atomicity" (operations guaranteed to execute without interference from another CPU/process). Hardware can provide some help with this.
E.g., "test and set lock" (TSL) instruction: TSL registerX, lockVar
(1) copies lockVar to registerX and (2) sets lockVar to non-zero, all as one atomic operation.
How to make this work is the hardware designers' problem!



Solution Using TSL Instruction, Continued
Proposed invariant: "lock is 0 exactly when no processes in their critical regions, and nonzero exactly when one process in its critical region." ("Exactly when" here means "if and only if".)
If this invariant holds, that means first requirement is met. (Does it hold? Next slide.) Others met too — well, except that it might be "unfair" (some process waits forever).
Is this a better solution? Simpler than Peterson's algorithm, but still involves busy-waiting. (Also depends on hardware features that *might* not be present, but these days almost all hardware has something similar.)





















	Bounded Buffer Problem — Solution	
	Shared variables:	
	<pre>buffer B(N); // empty, capacity N semaphore mutex(1); semaphore empty(N); semaphore full(0);</pre>	
de 23	Pseudocode for producer:	Pseudocode for consumer:
	while (true) {	while (true) {
	<pre>item = generate();</pre>	down(full);
	down (empty) ;	down(mutex);
	down(mutex);	item = get(B);
	<pre>put(item, B);</pre>	up(mutex);
	up(mutex);	up(empty);
	up(full);	use(item);
	\ }	}

