





Overview of I/O Hardware, Continued

 Interaction between CPU and controllers is via registers in controller (write to tell controller to do something, read to inquire about status), plus (sometimes) data buffer.

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 Very old example: Parallel port (connected to printers, etc.) has control register (example bit — linefeed), status register (example bit — busy), data register (one byte of data). These map onto the wires connecting the device to the CPU.





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Programmed I/O
Basic idea: Program tells controller what to do and busy-waits until it says it's done.
Simple but potentially inefficient — for the system as a whole, anyway. But a good choice if wait time is small.

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Interrupt Handlers
Background: Something at one of the higher levels has initiated an I/O operation and blocked itself (e.g., using a semaphore). When operation completes, interrupt handler is run.
Interrupt handler must:

Save state of current process so it can be restarted.
Deal with interrupt — acknowledge it (to interrupt controller), run interrupt service procedure to get info from device controller's registers/buffers.
Unblock requesting process.
Choose next process to run — maybe process that requested I/O, maybe interrupted process, maybe another — and do context switch.



Access to devices provided by special files (normally in /dev/*), to provide uniform interface for callers. Two categories, block and character. Each defines interface (set of functions) to device driver. Associated with each special file are major and minor device numbers, with major device number used to locate specific function. (Look at some output of ls -l /dev.)

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• Streams provide additional layer of abstraction for callers — can interface to files, terminals, etc. (This is what you access with *scanf, *printf.)

