









Proposed Solution — TSL Instruction
A key problem in concurrent algorithms is the idea of "atomicity" (operations guaranteed to execute without interference from another CPU/process). Hardware can provide some help with this.
E.g., "test and set lock" (TSL) instruction: TSL registerX, lockVar
(1) copies lockVar to registerX and (2) sets lockVar to non-zero, all as one atomic operation.
How to make this work is the hardware designers' problem!



Slide 7







