



Slide 2







Program Relocation At the machine-instruction level, references to memory are in terms of an absolute number. Compilers/assemblers can generate these only by making assumption about where program will reside in memory. In the very early days, programs started at 0, so no problem. Now they hardly ever do, so we need a way to relocate programs — when loaded, or "on the fly". "On the fly" relocation uses MMU (memory management unit) — which can provide both program relocation and memory protection. Logically between CPU and memory, physically usually part of CPU. A simple scheme — base and limit registers (described in text). When do values in them need to change?



















