





Mutual Exclusion Problem, Continued
We'll look at various solutions (some correct, some not):

Using only hardware features always present (some notion of shared variable).
Using optional hardware features.
Using "synchronization primitives" (abstractions that help solve this and other problems).

Recall that a correct solution

Must work for more than one CPU.
Must work even in the face of unpredictable context switches — whatever we're doing, another process can pull the rug out from under us between "atomic operations" (machine instructions).

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Proposed Solution — Simple Lock Variable

• Shared variables:
    int lock = 0;
Pseudocode for each process:
    while (true) {
        while (lock != 0);
        lock = 1;
        do_ar();
        lock = 0;
        do_non_cr();
    }
• Does it work? reviewing the criteria ... No.
```





mathematical "proof of correctness" of the algorithm.

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