







## "WSClock" Algorithm

- Idea efficient-to-implement variation of previous algorithm, based on circular list of pages-in-memory for process. (Carr and Hennessy.)
- Implementation like previous algorithm, but when we need to pick a page to replace, go around the circle and:
  - If R=1, update time of last use. Compute time since last use.
  - If time since last use is more than  $\tau$  and M = 1, schedule I/O to write this page out (so it can maybe be replaced next time M bit will be cleared when I/O completes). No need to block yet, though.
  - If time since last use is more than  $\tau$  and M = 0, replace this page.

The idea is to go around the circle until we find a page to replace, then stop. (If we get all the way around the circle, we'll pick some page with M=0.)

 How good is this? Makes good choices, practical to implement, apparently widely used in practice.



- Intuitively obvious that more memory leads to fewer page faults, right? Not always!
- Counterexample "Belady's anomaly", sparked interest in modeling page replacement algorithms.
- Modeling based on simplified version of reality one process only, known inputs. Can then record "reference string" of pages referenced.
- Given reference string, p.r.a., and number of page frames, we can calculate number of page faults.
- How is this useful? can compare different algorithms, and also determine if a given algorithm is a "stack algorithm" (more memory means fewer page faults).
- Slide 6











