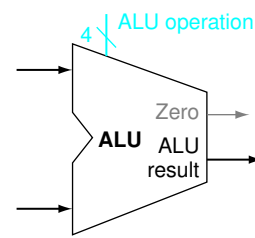


a. Registers



b. ALU