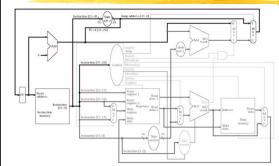
Datapath Conclusion 3-28-2003 **Opening Discussion** ■ What did we talk about last class? ■ Have you seen anything interesting in the news? **Designing Datapaths and Control** ■ Complex instructions are not always faster. Increasing number of transistors, faster computers, and better design tools have drastically changed the way that datapaths and controls are done. Fig 5.51

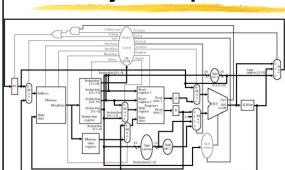
Chapter Review

What we are going to do now is go back over the main things that we have discussed in this chapter. There is a bit of a twist to this though. You will be doing most of the talking.

The Single Cycle Datapath



The Multicycle Datapath



Start Millor 10 Millo

Our Microprogram

Label	ALU Co	SRC1	SRC2	Reg Co	Memory	PCWrite	Seq
Fetch	Add	PC	4		Read PC	ALU	Seq
	Add	PC	Extshft	Read			Dispatch 1
Mem1	Add	Α	Extend				Dispatch 2
LW2					Read ALU		Seq
				Write MDR			Fetch
SW2					Write ALU		Fetch
Rformat1	Func code	Α	В				Seq
				Write ALU			Fetch
BEQ1	Subt	Α	В			ALUOut-	Fetch
						cond	
JUMP1						Jump	Fetch
						address	

Minute Essay

- Next class we will be starting to talk about pipelining with Chapter 6. That chapter further elaborates on datapaths, adding more complexities that are used in modern implementations. What questions do you have from this chapter that you need answered before that?
- Remember that there will be a quiz at the beginning of next class.